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## UNITED STATES PATENT AND TRADEMARK OFFICE

 COMMISSIONER FOR PATENTS  
 UNITED STATES PATENT AND TRADEMARK OFFICE  
 WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/080,424	MAY 31 2002	Takashi Hagino	YKI-0084

 23413  
 CANTOR COLBURN, LLP  
 55 GRIFFIN ROAD SOUTH  
 BLOOMFIELD, CT 06002


CONFIRMATION NO. 8277

## FORMALITIES LETTER



\*OC00000007649276\*

Date Mailed: 03/15/2002

## NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

FILED UNDER 37 CFR 1.53(b)

*Filing Date Granted*

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is missing.  
*A properly signed oath or declaration in compliance with 37 CFR 1.63, identifying the application by the above Application Number and Filing Date, is required.*
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(l) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.
- The balance due by applicant is \$ 130.

*A copy of this notice **MUST** be returned with the reply.*

Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

06/04/2002 MBERHE 00000103 10080424

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**CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)**

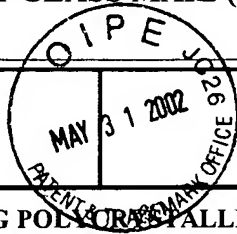
Applicant(s): **TAKASHI HAGINO, ET AL**

Docket No.

**YKI-0084**

Serial No.  
**10/080,424**

Filing Date  
**02/22/02**



Examiner

Group Art Unit  
**2812**

Invention: **METHOD AND MANUFACTURING POLYCRYSTALLINE SEMICONDUCTOR LAYERS AND THIN-FILM TRANSISTORS, AND LASER ANNEALING APPARATUS**

I hereby certify that this **RESPONSE TO NOTICE TO FILE MISSING PARTS AND POSTCARD**  
(Identify type of correspondence)

is being deposited with the United States Postal Service as first class mail in an envelope addressed to: The

Assistant Commissioner for Patents, Washington, D.C. 20231 on **MAY 13, 2002**  
(Date)

**NIDIA M. DERAS**

(Typed or Printed Name of Person Mailing Correspondence)

  
(Signature of Person Mailing Correspondence)

**Note: Each paper must have its own certificate of mailing.**

**Response To Notice To File Missing Parts Of Application  
Filing Date Granted (PTO-1533)(Large Entity)**

Docket No.  
YKI-0084

In Re Application Of: TAKASHI HAGINO, ET AL



Serial No.  
10/080,424

Filing Date  
02/22/2002

Examiner

Group Art Unit  
2812

Invention: **METHOD FOR MANUFACTURING POLYCRYSTALLINE SEMICONDUCTOR LAYERS  
THIN-FILM TRANSISTORS, AND LASER ANNEALING APPARATUS**

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Box Missing Parts

This is a response to the Notice to File Missing Parts of Application - Filing Date Granted (PTO-1533) mailed on  
MARCH 15, 2002  
Date

Enclosed herewith for filing are the following:

- ☒ A copy of the Notice to File Missing Parts of Application - Filing Date Granted (PTO-1533). **(REQUIRED)**
- ☒ An oath or declaration in compliance with 37 CFR 1.63, including residence information and identifying the application by the above Application Number and Filing Date.
- ☐ A properly signed oath or declaration in compliance with 37 CFR 1.63, identifying the application by the above Application Number and Filing Date.
- ☐ An oath or declaration in compliance with 37 CFR 1.63 listing the names of all inventors and signed by the omitted inventor(s), identifying this application by the above Application Number and Filing Date.
- ☐ A verified English translation of the non-English language application papers as originally filed. It is requested that this translation be used as the copy for examination purposes in the United States Patent and Trademark Office.
- ☐ Other (list):

**Response To Notice To File Missing Parts Of Application  
Filing Date Granted (PTO-1533)(Large Entity)**

Docket No.  
**YKI-0084**

In Re Application Of: **TAKASHI HAGINO, ET AL**

Serial No.  
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**2812**

Invention: **METHOD FOR MANUFACTURING POLYCRYSTALLINE SEMICONDUCTOR LAYERS  
THIN-FILM TRANSISTORS, AND LASER ANNEALING APPARATUS**

**TO THE ASSISTANT COMMISSIONER FOR PATENTS:**

**Box Missing Parts**

☒ Completion of application fees as calculated below:

☐ Utility application filing fee

☐ Design application filing fee

☐ Total number of independent claims =

☐ Total number of claims =

☐ Multiple dependent claims

☒ Surcharge for late payment of filing fee and/or late filing of original declaration or oath **\$130.00**

☐ Petition and fee for filing by other than all the inventors or a person not the inventor

☐ Fee for processing an application filed with a non-English language specification

☐ Fee for processing and retention of application

Total completion of application fees **\$130.00**

This is a request under the provisions of 37 CFR 1.136(a) to extend the period for filing a response to the above-identified Notice to File Missing Parts of Application. The requested extension is as follows (check time period desired). If an additional time extension is required, please consider this a petition therefor.

☐ One month ☐ Two months ☐ Three months ☐ Four months ☐ Five months

from: \_\_\_\_\_ until: \_\_\_\_\_  
Date Date

Total time extension fees

Total fees due **\$130.00**

**Response To Notice To File Missing Parts Of Application  
Filing Date Granted (PTO-1533) (Large Entity)**

Docket No.  
**YKI-0084**

In Re Application Of: **TAKASHI HAGINO, ET AL**

Serial No.  
**10/080,424**

Filing Date  
**02/22/2002**

Examiner

Group Art Unit  
**2812**

Invention: **METHOD FOR MANUFACTURING POLYCRYSTALLINE SEMICONDUCTOR LAYERS  
THIN-FILM TRANSISTORS, AND LASER ANNEALING APPARATUS**

**TO THE ASSISTANT COMMISSIONER FOR PATENTS:**

**Box Missing Parts**

The fee of **\$130.00** is to be paid as follows:

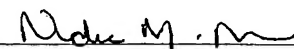
- ☒ A check in the amount of the fee is enclosed.
- ☐ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account No.  
A duplicate copy of this sheet is enclosed.
- ☒ If an additional extension of time is required, please consider this a petition therefor and charge any additional fees which may be required to Deposit Account No. **06-1130**  
A duplicate copy of this sheet is enclosed.

  
Signature

**LISA A. BONGIOVI  
REGISTRATION NO. 48,933  
CUSTOMER NO. 23413**

Dated: **MAY 13, 2002**

I certify that this document and fee is being deposited on **5/13/02** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

  
Signature of Person Mailing Correspondence

**NIDIA M. DERAS**

Typed or Printed Name of Person Mailing Correspondence

CC: